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Publications | Research

John L. Hennessy. Automated design procedures for operating systems and microprocessors, RISC, FLASH multiprocessor. Hennessy was born in Huntington, New York, in 1952. He received his Ph.D. in computer science in 1977 from the State University of New York at Stony Brook (now Stony Brook University).

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Mark Horowitz is part of Stanford Profiles, official site for faculty, postdocs, students and staff information (Expertise, Bio, Research, Publications, and more). The site facilitates research and collaboration in academic endeavors.

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effectively on large one-dimensional arrays of data called vectors. This is in contrast to scalar processors, whose instructions operate on single data items only, and in contrast to some of those same scalar ...

Vector processor - Wikipedia

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History of computing hardware - Wikipedia

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Computer architecture researchers have been investigating energy consumption for decades, especially to be able to deliver state-of-the-art energy efficient processors. Machine learning researchers, on the other hand, have been mainly focused on producing high accurate models without considering energy consumption as an important factor [18] .

Estimation of energy consumption in machine learning ...

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